

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application for:

Takashi Kumamoto

Application No.: 10/663,485

Filed: 09/15/2003

Patent No.: 7,138,709 B2

Issued: 11/21/2006

For: MICROELECTRONIC PACKAGE
ARRAY

Examiner: Nadav, Ori

Art Group: 2811

Confirmation No. 2427

Attn: Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION

Dear Sir:

Upon review of the above-referenced Letters Patent, Applicants noted an error that is the mistake of the Applicants. The error is as follows:

Column 4

Lines 32-33, "...intermediate substrate **30**..." should read --...intermediate substrate blank **30**...--.

Line 39, "...substrate blank **30**,..." should read --...intermediate substrate blank **30**,...--.

Column 5

Line 1, "...intermediate substrate **30**..." should read --...intermediate substrate blank **30**...--.

Lines 14-15, "...to over lay..." should read --...to overlay...--.

Lines 61-62, "...second carrier substrate **112**,..." should read --...second carrier substrate **112'**,...--.

Column 6

Line 15, "...**102**" should read --...**102**.

Column 7

Lines 26-27, "...microelectronic package **92**..." should read --...microelectronic package array **92**...--.

Included with this submission is a Certificate of Correction and it is respectfully requested that a Certificate of Correction be issued. This request for correction is made under the provisions of 37 CFR 1.323.

Payment by Deposit Account No. 500393 is included in the amount of \$100.00 for the fee as set forth in 37 CFR § 1.20(a).

Respectfully submitted,
SCHWABE, WILLIAMSON & WYATT, P.C.

Dated: 02/18/2008

/James J. Namiki/
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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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ISSUE DATE : 11/21/2006

INVENTOR(S) : Takashi Kumamoto

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

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Column 7

Lines 26-27, "...microelectronic package 92..." should read "...microelectronic package array 92...--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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